lvor Catt developed and patented some ideas Wafer on Scale Integration (WSI) in 1972, and published his work in Wireless World in 1981, after his articles on the topic were rejected by academic journals. The technique, christened Catt Spiral, was designed to enable the use of partially faulty integrated chips (called partials), which were otherwise discarded by manufacturer s.

In 1980 а European Commission semiconducto r technology committee has awarded \$11.6m to UKbased Anamartic Ltd, Siemens AG and Bull SA, for the development of waferscale technology (CI No 1,488). The award will be spread over four years and is part of



Click on the Image For Detail





the Commission's efforts to support European companies in the development of leading edge technologies.

In mid-1980s, British а company Anamartic, by funded Tandem Computers and Sir Clive Sinclair among others, announced plans to manufacture microchips ("superchips") based on Catt's technology. The approach was reported to be revolutionary at the time, with predictions that it would enable construction powerful of supercomputers from cheap, mass produced components, and cheaper and faster replacements for magnetic disk memories. Anamartic

introduced a



solid-state memory, the called Wafer Stack, based on the technology in 1989 and the device won Electronic Product's 'Product of Year the Award'. However the company could not ensure а large enough supply of silicon wafers, which were crucial for its chip manufacturin g, and folded in 1992. Waferscale technology refers to the use of an entire semiconducto r wafer as the basic unit of the supercompon ent, rather than chips cut from the wafer. The supercompon ents will be used in a variety of intermediate storage devices and three the companies will concentrate on developing 4M-bit and 16M-bit dynamic hard

storage memory systems. Storage Capacity 80,160 or 240 Mbytes (unformatted) Standard Interface SCSi, singleended Access Time 1 millisecond average Transfer rate Asynchronou s 3.0 Mbytes/secon d maximum, 2.5 Mbytes/s sustained Synchronous 4.8 Mbytes/secon d maximum, 3.0 Mbytes/s sustained Block size setectabte 128 to 16384 bytes in increments of 2 SCSI buffer size 32768 bytes (50x512 byte blocks) Physical Size 8" disk drive form factor, 215.9 x 127 x 616 mm (8.5" x 5" x 24.3" Weight 15.0kg Cooling Fan cooled Models 80 Mbytes 160 Mbytes 240 Mbytes **Date :** 1982

Manufacture

r : Anamartic

This exhibit has a reference ID of **CH4619**. Please quote this reference ID in any communication with the Centre for Computing History.